

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE
SCANNED <i>AL</i> 3	Q.A. <i>AL</i> 3	

APPLICATION NO. 09/912064	CONT/PRIOR F	CLASS 257	SUBCLASS GEG	ART UNIT 2811 2826	EXAMINER Erdem
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Makoto Terui

Semiconductor chip package

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	666	257	777	778	782	783	666	723	
INTERNATIONAL CLASSIFICATION			724	780					
4	5								
1	2								
3	4								
5	6								
7	8								
9	0								

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**TERMINAL
DISCLAIMER**

- ☐ The term of this patent subsequent to _____ has been disclaimed.
- ☐ The term of this patent not extend beyond the exp of U.S Patent. No. _____
- ☐ The terminal _____ month of this patent have been disclaimed.

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DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O. G.
<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div>			NOTICE OF ALLOWANCE MAILED	
			<div>_____ (Amount Due) _____ (Date Paid)</div>	
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